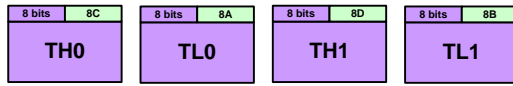
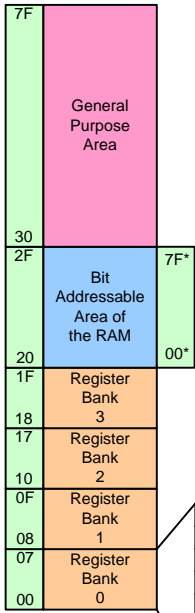


Timer Control Registers

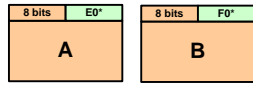


Timer/Counter Registers

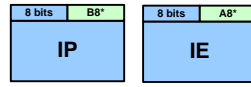


Internal RAM

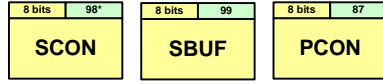
R7	07
R6	06
R5	05
R4	04
R3	03
R2	02
R1	01
R0	00



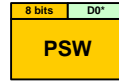
Math Registers



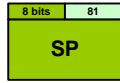
Interrupt Registers



Serial Data Registers



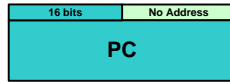
Flags



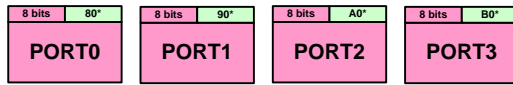
Stack Pointer



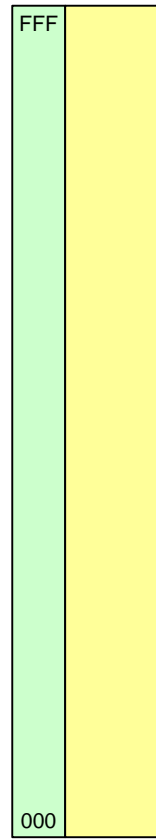
Data Pointer



Program Counter



PORT Latches



Internal ROM

* indicates Bit Addressable